NAME

make - GNU make utility to maintain groups of programs

SYNOPSIS

make [-f makefile] [option] ... target ...

WARNING

This man page is an extract of the documentation of \underline{GNU} make. . It is updated only occasionally, because the GNU project does not use nroff. For complete, current documentation, refer to the Info file make.info which is made from the Texinfo source file make.texinfo.

DESCRIPTION

The purpose of the \underline{make} utility is to determine automatically which pieces of a large program need to be recompiled, and issue the commands to recompile them. The manual describes the GNU implementation of \underline{make} , which was written by Richard Stallman and Roland McGrath. Our examples show C programs, since they are most common, but you can use \underline{make} with any programming language whose compiler can be run with a shell command. In fact, \underline{make} is not limited to programs. You can use it to describe any task where some files must be updated automatically from others whenever the others change.

To prepare to use \underline{make} , you must write a file called the $\underline{makefile}$ that describes the relationships among files in your program, and the states the commands for updating each file. In a program, typically the executable file is updated from object files, which are in turn made by compiling source files.

Once a suitable makefile exists, each time you change some source files, this simple shell command:

make

suffices to perform all necessary recompilations. The <u>make</u> program uses the makefile data base and the lastmodification times of the files to decide which of the files need to be updated. For each of those files, it issues the commands recorded in the data base.

<u>make</u> executes commands in the <u>makefile</u> to update one or more target <u>names</u>, where <u>name</u> is typically a program. If no -f option is present, <u>make</u> will look for the makefiles <u>GNU-makefile</u>, <u>makefile</u>, and <u>Makefile</u>, in that order.

Normally you should call your makefile either $\underline{makefile}$ or $\underline{Makefile}$. (We recommend $\underline{Makefile}$ because it appears prominently near the beginning of a directory listing, right

1

near other important files such as \underline{README} .) The first name checked, $\underline{GNUmakefile}$, is not recommended for most makefiles. You should use this name if you have a makefile that is specific to $\underline{GNUmake}$, and will not be understood by other versions of \underline{make} . If $\underline{makefile}$ is '-', the standard input is read.

 \underline{make} updates a target if it depends on prerequisite files that have been modified since the target was last modified, or if the target does not exist.

OPTIONS

-b

-m These options are ignored for compatibility with other versions of make.

-C dir

Change to directory <u>dir</u> before reading the makefiles or doing anything else. If multiple -C options are specified, each is interpreted relative to the previous one: - C / -C etc is equivalent to -C /etc. This is typically used with recursive invocations of <u>make</u>.

- -d Print debugging information in addition to normal processing. The debugging information says which files are being considered for remaking, which file-times are being compared and with what results, which files actually need to be remade, which implicit rules are considered and which are applied---everything interesting about how make decides what to do.
- -e Give variables taken from the environment precedence over variables from makefiles.
- -f $\frac{file}{Use}$ file as a makefile.
- -i Ignore all errors in commands executed to remake files.

-I dir

Specifies a directory \underline{dir} to search for included makefiles. If several -I options are used to specify several directories, the directories are searched in the order specified. Unlike the arguments to other flags of <u>make</u>, directories given with -I flags may come directly after the flag: -I<u>dir</u> is allowed, as well as -I <u>dir</u>. This syntax is allowed for compatibility with the C preprocessor's -I flag.

-j <u>jobs</u>

2

Specifies the number of jobs (commands) to run simultaneously. If there is more than one -j option, the last one is effective. If the -j option is given without an argument, <u>make</u> will not limit the number of jobs that can run simultaneously.

-k Continue as much as possible after an error. While the target that failed, and those that depend on it, cannot be remade, the other dependencies of these targets can be processed all the same.

-1

-l <u>load</u>

Specifies that no new jobs (commands) should be started if there are others jobs running and the load average is at least 10 ad (a floating-point number). With no argument, removes a previous load limit.

-n Print the commands that would be executed, but do not execute them.

-o file

Do not remake the file <u>file</u> even if it is older than its dependencies, and do not remake anything on account of changes in <u>file</u>. Essentially the file is treated as very old and its rules are ignored.

- -p Print the data base (rules and variable values) that results from reading the makefiles; then execute as usual or as otherwise specified. This also prints the version information given by the -v switch (see below). To print the data base without trying to remake any files, use make -p -f/d e v/n ull.
- -q "Question mode". Do not run any commands, or print anything; just return an exit status that is zero if the specified targets are already up to date, nonzero otherwise.
- -r Eliminate use of the built-in implicit rules. Also clear out the default list of suffixes for suffix rules.
- -s Silent operation; do not print the commands as they are executed.
- -S Cancel the effect of the -k option. This is never necessary except in a recursive <u>make</u> where -k might be inherited from the top-level <u>make</u> via MAKEFLAGS or if you set -k in MAKEFLAGS in your environment.

- -t Touch files (mark them up to date without really changing them) instead of running their commands. This is used to pretend that the commands were done, in order to fool future invocations of make.
- -v Print the version of the <u>make</u> program plus a copyright, a list of authors and a notice that there is no warranty.
- -w Print a message containing the working directory before and after other processing. This may be useful for tracking down errors from complicated nests of recursive make commands.

-W file

Pretend that the target <u>file</u> has just been modified. When used with the -n flag, this shows you what would happen if you were to modify that file. Without -n, it is almost the same as running a <u>touch</u> command on the given file before running <u>make</u>, except that the modification time is changed only in the imagination of <u>make</u>.

SEE ALSO

The GNU Make Manual

BUGS

See the chapter 'Problems and Bugs' in The GNU Make Manual.

AUTHOR

This manual page contributed by Dennis Morse of Stanford University. It has been reworked by Roland McGrath.